

DETAILED ACTION

Response to Amendment

1. The amendment After Final filed May 16, 2008 has been entered and claims 1-9, 11 and 15-31 have been cancelled.

EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Shin Hung on May 29, 2008.

The application has been amended as follows:

In the Claims:

In claim 10, line 16, delete -- ; -- and insert “, said isolation devices including transistors, each having a gate oxide corresponding to said thick gate oxide portion;” after devices.

Allowable Subject Matter

3. Claims 10 and 12-14 are allowed.
4. The following is an examiner's statement of reasons for allowance: Peng et al. (“Peng”) US PG-Pub 2004/0156234, Yang (USPN 6,295,226), Challa (USPN 5,323,351) and Tran et al. (“Tran”) US PG-Pub 2003/0052320, closely related references, teach the following:

Peng discloses anti-fuse memory array comprising: a plurality of anti-fuse transistors arranged in rows and columns, each anti-fuse transistor including a polysilicon gate over a channel region in a substrate; a diffusion region; a variable thickness gate oxide between the polysilicon gate and the substrate, the variable thickness gate oxide having a thick gate oxide portion, and a thin gate oxide portion; and bitlines coupled to the diffusion regions of a column of anti-fuse transistors.

Tran discloses a sense amplifier coupled to a pair of bitlines through isolation devices. See also Yang and Challa.

However, amended claim 10 requires isolation devices including transistors, each having a gate oxide corresponding to said thick gate oxide portion of the anti-fuse transistor.

The above limitations in combination with other claim limitations are not taught or fairly suggested by the prior art nor would it be obvious to modify the references of record so as to yield the memory array of the instant application.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ahmed Sefer whose telephone number is (571)272-1921.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sue Purvis can be reached on (571) 272-1236.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/A. Sefer/
Primary Examiner
Art Unit 2826